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 Kavi, K.M.; Hyong-Shik Kim; Arul, J.; Hurson, A.R.;
[Parallel Architectures, Algorithms, and Networks, 1999. \(I-SPAN '99\) Proceedings, Fourth International Conference on](#)
 23-25 June 1999 Page(s):138 - 143
 Digital Object Identifier 10.1109/ISPAN.1999.778930
[AbstractPlus](#) | Full Text: [PDF](#)(80 KB) IEEE CNF
[Rights and Permissions](#)
- ☐ **27. Context-switching techniques for decoupled multithreaded processors**
 Kreuzinger, J.; Ungerer, T.;
[EUROMICRO Conference, 1999. Proceedings, 25th](#)
 Volume 1, 8-10 Sept. 1999 Page(s):248 - 251 vol.1
 Digital Object Identifier 10.1109/EURMIC.1999.794476
[AbstractPlus](#) | Full Text: [PDF](#)(40 KB) IEEE CNF
[Rights and Permissions](#)
- ☐ **28. Locking with different granularities for reads and writes in an MVM system**
 Bodorik, P.; Jutta, D.; Slonim, J.; Agarwal, A.;
[Database Engineering and Applications, 1999. IDEAS '99. International Symposium Proceedings](#)
 2-4 Aug. 1999 Page(s):311 - 320
 Digital Object Identifier 10.1109/IDEAS.1999.787281
[AbstractPlus](#) | Full Text: [PDF](#)(100 KB) IEEE CNF
[Rights and Permissions](#)
- ☐ **29. Timed Petri net models of multithreaded multiprocessor architectures**
 Govindarajan, R.; Suciu, F.; Zuberek, W.M.;
[Petri Nets and Performance Models, 1997., Proceedings of the Seventh International Workshop on](#)
 3-6 June 1997 Page(s):153 - 162
 Digital Object Identifier 10.1109/PNPM.1997.595546
[AbstractPlus](#) | Full Text: [PDF](#)(1016 KB) IEEE CNF
[Rights and Permissions](#)
- ☐ **30. Language support for synchronous parallel critical sections**
 Kessler, C.W.; Seidl, H.;
[Advances in Parallel and Distributed Computing, 1997. Proceedings](#)
 19-21 March 1997 Page(s):92 - 99
 Digital Object Identifier 10.1109/APDC.1997.574018

[AbstractPlus](#) | Full Text: [PDF\(944 KB\)](#) IEEE CNF
[Rights and Permissions](#)

- ☐ **31. ATM RendezView: multipoint conferencing on ATM**
Smith, K.; Pretty, R.;
[Multimedia Computing and Systems '97. Proceedings., IEEE International Conference on](#)
3-6 June 1997 Page(s):534 - 540
Digital Object Identifier 10.1109/MMCS.1997.609766

[AbstractPlus](#) | Full Text: [PDF\(664 KB\)](#) IEEE CNF
[Rights and Permissions](#)
- ☐ **32. Automated, full load motor testing at production speeds**
Chrisco, R.M.;
[Electrical Insulation Conference, 1997, and Electrical Manufacturing & Coil Winding Conference, P](#)
22-25 Sept. 1997 Page(s):337 - 343
Digital Object Identifier 10.1109/EEIC.1997.651133

[AbstractPlus](#) | Full Text: [PDF\(1052 KB\)](#) IEEE CNF
[Rights and Permissions](#)
- ☐ **33. MiThOS-a real-time micro-kernel threads operating system**
Mueller, F.; Rustagi, V.; Baker, T.P.;
[Real-Time Systems Symposium, 1995. Proceedings., 16th IEEE](#)
5-7 Dec. 1995 Page(s):49 - 53
Digital Object Identifier 10.1109/REAL.1995.495195

[AbstractPlus](#) | Full Text: [PDF\(508 KB\)](#) IEEE CNF
[Rights and Permissions](#)
- ☐ **34. Thread prioritization: a thread scheduling mechanism for multiple-context parallel processo**
Fiske, S.; Dally, W.J.;
[High-Performance Computer Architecture, 1995. Proceedings. First IEEE Symposium on](#)
22-25 Jan. 1995 Page(s):210 - 221
Digital Object Identifier 10.1109/HPCA.1995.386541

[AbstractPlus](#) | Full Text: [PDF\(784 KB\)](#) IEEE CNF
[Rights and Permissions](#)
- ☐ **35. APRIL: a processor architecture for multiprocessing**
Agarwal, A.; Lim, B.-H.; Kranz, D.; Kubiawicz, J.;
[Computer Architecture, 1990. Proceedings, 17th Annual International Symposium on](#)
28-31 May 1990 Page(s):104 - 114
Digital Object Identifier 10.1109/ISCA.1990.134498

[AbstractPlus](#) | Full Text: [PDF\(956 KB\)](#) IEEE CNF
[Rights and Permissions](#)
- ☐ **36. Friendly and efficient message handling**
Bhoedjang, R.A.F.; Langendoen, K.G.;
[System Sciences, 1996., Proceedings of the Twenty-Ninth Hawaii International Conference on,](#)
Volume 1, 3-6 Jan. 1996 Page(s):121 - 130 vol.1
Digital Object Identifier 10.1109/HICSS.1996.495455

[AbstractPlus](#) | Full Text: [PDF\(928 KB\)](#) IEEE CNF
[Rights and Permissions](#)
- ☐ **37. The Parity protected, multithreaded register files on the 90-nm Itanium microprocessor**
Fetzer, E.S.; Dahle, D.; Little, C.; Safford, K.;
[Solid-State Circuits, IEEE Journal of](#)
Volume 41, Issue 1, Jan. 2006 Page(s):246 - 255
Digital Object Identifier 10.1109/JSSC.2005.859884

[AbstractPlus](#) | Full Text: [PDF\(1056 KB\)](#) IEEE JNL
[Rights and Permissions](#)

- ☐ **13. Design and implementation of a content-aware switch using a network processor**
Zhao, L.; Luo, Y.; Bhuyan, L.; Ravi Iyer;
[High Performance Interconnects, 2005. Proceedings. 13th Symposium on](#)
17-19 Aug. 2005 Page(s):79 - 85
Digital Object Identifier 10.1109/CONNECT.2005.16
[AbstractPlus](#) | Full Text: [PDF\(168 KB\)](#) IEEE CNF
[Rights and Permissions](#)

- ☐ **14. Clustered DBMS scalability under unified Ethernet fabric**
Kant, K.; Sahoo, A.;
[Parallel Processing, 2005. ICPP 2005. International Conference on](#)
14-17 June 2005 Page(s):416 - 423
Digital Object Identifier 10.1109/ICPP.2005.24
[AbstractPlus](#) | Full Text: [PDF\(168 KB\)](#) IEEE CNF
[Rights and Permissions](#)

- ☐ **15. Loop scheduling for multithreaded processors**
Dimitriou, G.; Polychronopoulos, C.;
[Parallel Computing in Electrical Engineering, 2004. International Conference on](#)
7-10 Sept. 2004 Page(s):361 - 366
Digital Object Identifier 10.1109/PCEE.2004.1335651
[AbstractPlus](#) | Full Text: [PDF\(343 KB\)](#) IEEE CNF
[Rights and Permissions](#)

- ☐ **16. Weaving threads of actions for personal information management**
Shimakawa, H.; Kaede, S.; Nakamura, E.; Azuma, S.;
[Communications and Information Technology, 2004. ISCT 2004. IEEE International Symposium on](#)
Volume 2, 26-29 Oct. 2004 Page(s):787 - 792 vol.2
Digital Object Identifier 10.1109/ISCT.2004.1413824
[AbstractPlus](#) | Full Text: [PDF\(699 KB\)](#) IEEE CNF
[Rights and Permissions](#)

- ☐ **17. Dynamic real-time reconfiguration on a multithreaded Java-microcontroller**
Pfeffer, M.; Ungerer, T.;
[Object-Oriented Real-Time Distributed Computing, 2004. Proceedings. Seventh IEEE International](#)
2004 Page(s):86 - 92
Digital Object Identifier 10.1109/ISORC.2004.1300333
[AbstractPlus](#) | Full Text: [PDF\(1356 KB\)](#) IEEE CNF
[Rights and Permissions](#)

- ☐ **18. Queue management for QoS provision build on network processor**
Wenjiang Zhou; Chuang Lin; Yin Li; Zhangxi Tan;
[Distributed Computing Systems, 2003. FTDCS 2003. Proceedings. The Ninth IEEE Workshop on](#)
28-30 May 2003 Page(s):219 - 224
Digital Object Identifier 10.1109/FTDCS.2003.1204336
[AbstractPlus](#) | Full Text: [PDF\(274 KB\)](#) IEEE CNF
[Rights and Permissions](#)

- ☐ **19. Fast context switching by hierarchical task allocation and reconfigurable cache**
Tanaka, K.;
[Innovative Architecture for Future Generation High-Performance Processors and Systems, 2003](#)
17 July 2003 Page(s):20 - 29
Digital Object Identifier 10.1109/IWIA.2003.1262779
[AbstractPlus](#) | Full Text: [PDF\(6163 KB\)](#) IEEE CNF
[Rights and Permissions](#)

- ☐ **20. Multi-thread IO in high quality digital broadcasting system**
Shuo Wang; Jiangling Zhang; Dan Feng;

- ☐ **38. Effects of multithreading on cache performance**
Kwak, H.; Lee, B.; Hurson, A.R.; Suk-Han Yoon; Woo-Jong Hahn;
[Computers, IEEE Transactions on](#)
Volume 48, Issue 2, Feb. 1999 Page(s):176 - 184
Digital Object Identifier 10.1109/12.752659
[AbstractPlus](#) | [References](#) | Full Text: [PDF](#)(240 KB) IEEE JNL
[Rights and Permissions](#)
- ☐ **39. A single-chip programmable platform based on a multithreaded processor and configurable**
Young-Don Bae; Seong-Il Park; In-Cheol Park;
[Solid-State Circuits, IEEE Journal of](#)
Volume 38, Issue 10, Oct. 2003 Page(s):1703 - 1711
Digital Object Identifier 10.1109/JSSC.2003.817259
[AbstractPlus](#) | [References](#) | Full Text: [PDF](#)(715 KB) IEEE JNL
[Rights and Permissions](#)
- ☐ **40. Dynamic helper threaded prefetching on the Sun UltraSPARC/spl reg/ CMP processor**
Jiwei Lu; Das, A.; Wei-Chung Hsu; Khoa Nguyen; Abraham, S.G.;
[Microarchitecture, 2005. MICRO-38. Proceedings. 38th Annual IEEE/ACM International Symposium](#)
12-16 Nov. 2005 Page(s):12 pp.
Digital Object Identifier 10.1109/MICRO.2005.18
[AbstractPlus](#) | Full Text: [PDF](#)(352 KB) IEEE CNF
[Rights and Permissions](#)
- ☐ **41. Low power network processor design using clock gating**
Yan Luo; Jia Yu; Jun Yang; Laxmi Bhuyan;
[Design Automation Conference, 2005. Proceedings. 42nd](#)
13-17 June 2005 Page(s):712 - 715
[AbstractPlus](#) | Full Text: [PDF](#)(349 KB) IEEE CNF
[Rights and Permissions](#)
- ☐ **42. Multi-threading and remote latency in software DSMs**
Thitikamol, K.; Keleher, P.;
[Distributed Computing Systems, 1997.. Proceedings of the 17th International Conference on](#)
27-30 May 1997 Page(s):296 - 304
Digital Object Identifier 10.1109/ICDCS.1997.598057
[AbstractPlus](#) | Full Text: [PDF](#)(1084 KB) IEEE CNF
[Rights and Permissions](#)
- ☐ **43. Quantitive studies of data-locality sensitivity on the EARTH multithreaded architecture: preli**
Xin-Min Tian; Nemawarkar, S.; Gao, G.R.; Hum, H.; Maquelin, O.; Sodan, A.; Theobald, K.;
[High Performance Computing, 1996. Proceedings. 3rd International Conference on](#)
19-22 Dec. 1996 Page(s):362 - 367
Digital Object Identifier 10.1109/HIPC.1996.565848
[AbstractPlus](#) | Full Text: [PDF](#)(596 KB) IEEE CNF
[Rights and Permissions](#)
- ☐ **44. Integrating polling, interrupts, and thread management**
Langendoen, K.; Romein, J.; Bhoedjang, R.; Bal, H.;
[Frontiers of Massively Parallel Computing, 1996. Proceedings 'Frontiers '96'. Sixth Symposium on](#)
27-31 Oct. 1996 Page(s):13 - 22
Digital Object Identifier 10.1109/FMPC.1996.558057
[AbstractPlus](#) | Full Text: [PDF](#)(1092 KB) IEEE CNF
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[Computer Networks and Mobile Computing, 2003. ICCNMC 2003. 2003 International Conference on](#)
20-23 Oct. 2003 Page(s):442 - 445

[AbstractPlus](#) | Full Text: [PDF](#)(207 KB) IEEE CNF
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21. Analytical bounds on the threads in IXP1200 network processor

Ramakrishna, S.T.G.S.; Jamadagni, H.S.;
[Digital System Design, 2003. Proceedings. Euromicro Symposium on](#)
1-6 Sept. 2003 Page(s):426 - 429
Digital Object Identifier 10.1109/DSD.2003.1231976

[AbstractPlus](#) | Full Text: [PDF](#)(267 KB) IEEE CNF
[Rights and Permissions](#)



22. Comparing analytical modeling with simulation for network processors: a case study

Gries, M.; Kulkarni, C.; Sauer, C.; Keutzer, K.;
[Design, Automation and Test in Europe Conference and Exhibition, 2003](#)
2003 Page(s):256 - 261 suppl.
Digital Object Identifier 10.1109/DATE.2003.1253838

[AbstractPlus](#) | Full Text: [PDF](#)(281 KB) IEEE CNF
[Rights and Permissions](#)



23. Lazy process switching

Liedtke, J.; Wenske, H.;
[Hot Topics in Operating Systems, 2001. Proceedings of the Eighth Workshop on](#)
20-22 May 2001 Page(s):15 - 18

[AbstractPlus](#) | Full Text: [PDF](#)(416 KB) IEEE CNF
[Rights and Permissions](#)



24. Multi-thread VLIW processor architecture for HDTV decoding

Hansoo Kim; Woo-Seung Yang; Myoung-Cheol Shin; Seung-Jai Min; Seong-Ok Bae; In-Cheol Park
[Custom Integrated Circuits Conference, 2000. CICC. Proceedings of the IEEE 2000](#)
21-24 May 2000 Page(s):559 - 562
Digital Object Identifier 10.1109/CICC.2000.852730

[AbstractPlus](#) | Full Text: [PDF](#)(384 KB) IEEE CNF
[Rights and Permissions](#)



25. Monitoring distributed real-time activities in DCOM

Mock, M.; Gergeleit, M.; Nett, E.;
[Object-Oriented Real-Time Distributed Computing, 2000. \(ISORC 2000\) Proceedings. Third IEEE I](#)
[Symposium on](#)
15-17 March 2000 Page(s):26 - 33
Digital Object Identifier 10.1109/ISORC.2000.839508

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